EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S94	9178	thread with (integrated circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/12/14 20:29
S95	351	thread with (integrated circuit) and (interconnect$3 same thread)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/12/14 20:29
S96	54	thread with (integrated near circuit) and (interconnect$3 same thread)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/12/14 20:29
S97	3	S96 and (programmable near logic near device)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/12/14 20:30
S98	24	S96 and (control near logic)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/12/14 20:31
S99	1	interconnect$ with (thread same message) with (integrated near circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/12/14 21:17
S100	1	interconnect$3 with (thread same message) with (integrated near circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/12/14 21:17
S101	1	interconnect$3 with (thread same message) and (integrated near circuit) and (control near logic)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/12/14 21:18
S102	6	interconnect$3 with (thread same message) and (integrated near circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/12/14 21:18

S103	10	connect$3 with (thread same message) and (integrated near circuit) and (control near logic)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/12/14 21:21
S104	0	connect$3 with (thread near circuit) and (integrated near circuit) and (control near logic)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/12/14 21:25
S105	0	connect$3 with (thread near circuit) and (control near logic)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/12/14 21:25
S106	11	(thread near circuit) and (control near logic)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/12/14 21:25
S107	3	(thread near circuit) and (control near logic) and message	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/12/14 21:25

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